

Docket No.: 60188-585

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of : Customer Number: 20277

Kunisato YAMAOKA, et al. : Confirmation Number: 9664

Application No.: 10/620,614 : Group Art Unit: 2818

Filed: July 17, 2003 : Examiner: Ho, Hoai V.

For: FERROELECTRIC MEMORY DEVICE AND METHOD FOR READING DATA FROM

THE SAME

INFORMATION DISCLOSURE STATEMENT

Mail Stop Information Disclosure Statement Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached form PTO-1449. It is respectfully requested that the documents be expressly considered during the prosecution of this application, and that the documents be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed more than three months after the U.S. filing date AND after the mailing date of the first Office Action on the merits, but before the mailing date of a Final Rejection or Notice of Allowance.

In accordance with 37 CFR 1.17(p), please charge the fee of \$180.00 to Deposit Account No. 500417.

10/620,614

Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

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Date: December 15, 2004



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					APPLICANT Kunisato YAMAOKA, et al.					
	((PT	O-1449)		FILING DATE July 17, 2003		GROUP 2818			
U.S. PATENT DOCUMENTS										
EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Code2 (# known)		Publication Da MM-DD-YYY		Name of Patentee or Applicant of Cited Document		Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear		
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FOREIGN PATENT DOCUMENTS										
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		Yeon	Yeonbae chung et al., "A 3.3-V 4-Mb Nonvolatile Ferroelectric RAM with Selectively-Driven Double-Pulsed Plate Read/Write-Back Scheme," Symposium on VLSI Circuits Digest of Technical Papers, pp. 97-97, 1999.							
										
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^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.